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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30559

Laurence R. Simar, Jr., et al.

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman


Filed: October 31, 2000

Conf. No.: 9784

For: Microprocessor With Execution Packet Spanning Two or More Fetch Packets

CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

_____ May 23, 2005 _____
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>111 #2 (10 Pages)</u>
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u>(# Page)</u>
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Laurence R. Simar, Jr., et al.	
TITLE OF INVENTION: Microprocessor With Execution Packet Spanning Two or More Fetch Packets	
TI FILE NO.: TI-30559	DEPOSIT ACCT. NO.: 20-0668
FAXED: 5/23/05 (Mon) DUE: 5/22/05 (Sun) ATTY/SECY: RDM/reb	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/702,320 Filing Date: October 31, 2000	

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Texas Instruments Incorporated
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Simar et al

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

Filed: October 31, 2000

Docket: TI-30556

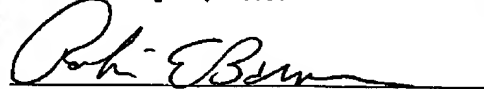
For: MICROPROCESSOR WITH EXECUTION PACKET SPANNING TWO OR MORE
FETCH PACKETS

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents
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UNDER 37 C.F.R. §1.6(b)**

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated February 22, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: